

### Remarks

This Preliminary Amendment cancels, without prejudice, claims 1-5 in the underlying PCT Application No. PCT/EP2004/050216 and adds new claims 6-11. The new claims, inter alia, conform the claims to United States Patent and Trademark Office rules and do not add any new matter to the application.

In accordance with 37 C.F.R. § 1.125(b), the Substitute Specification (including Abstract) contains no new matter. The amendments reflected in the Substitute Specification (including Abstract) are to conform the Specification and Abstract to United States Patent and Trademark Office rules or to correct informalities. As required by 37 C.F.R. §§ 1.121(b)(3)(ii) and 1.125(c), a Marked-Up Version of the Substitute Specification comparing the Specification of record and the Substitute Specification also accompanies this Preliminary Amendment. Approval and entry of the Substitute Specification (including Abstract) are respectfully requested.

The underlying PCT Application No. PCT/EP2004/050216 includes an International Search Report, dated September 15, 2004, a copy of which is included. The Search Report includes a list of documents that were considered by the Examiner in the underlying PCT application.

Applicant asserts that the subject matter of the present application is new, non-obvious, and useful. Prompt consideration and allowance of the application are respectfully requested.

Respectfully Submitted,

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# RECEIVER UNIT AND METHOD FOR RECEIVING ANALOG RECEIVED SIGNALS

## Field Of The Invention

The present invention relates to a receiver unit having at least two signal receivers for analog received signals according to digital or analog standard, an analog-digital converter for converting the analog received signals into digital received signals and

5 a signal processing unit for the digital received signals. The present invention additionally relates to a method for receiving analog received signals, particularly radio broadcast signals, using a receiver unit of this kind.

## Background Information

10 Conventional radio broadcast receivers for receiving amplitude-modulated (AM) and frequency-modulated (FM) received signals often have more than one signal receiver so as to receive, for example, an audio signal on a first channel and at the same time data on a second channel. The data may be transmitted, for example, in accordance with the Radio Data Standard (RDS) or in the Traffic Message Channel

15 (TMC). The several signal receivers may also be used to receive several radio broadcast standards such as, for example, Frequency Modulation (FM), Digital Audio Broadcast (~~DRBD~~DAB), Digital Radio Mondiale (DRM), In Band on Channel (IBoC) or Satellite Digital Audio Radio Service (SDARS) using a receiver unit. Here the simultaneous reception of data and audio signals is conceivable as well. The several

20 signal receivers may also be used to monitor the range of broadcast stations currently receivable. If the signal quality of a broadcast station in a standard decreases, it is possible to switch to a broadcast station having the same content as the first broadcast station.

25 The analog received signals are picked up by the antenna using signal receivers and following a band-pass filtering are amplified in a prestage. Using a mixer controlled by an oscillator, the band-pass filtered and amplified analog received signal is mixed to an intermediate frequency, is intermediate frequency-filtered and again amplified. The analog received signal preprocessed in this way in the signal receiver is

30 converted for each signal receiver in each case by one analog-digital converter into a digital received signal and is digitally processed further in a subsequent signal processing unit. For this purpose, the performance demands on the analog-digital

converter are relatively high. The analog-digital converters should have a high scanning speed, high dynamics and a power consumption that is as low as possible and if possible be able to be integrated in an integrated circuit together with other functions. The chip surface and power consumption currently required for the several  
5 analog-digital converter, however, is too high.

### Summary Of The Invention

An objective of the present invention therefore is ~~create~~to provide an improved receiver unit with which it is possible to reduce the chip surface, costs and power  
10 consumption for the analog-digital converters.

According to the present invention, the receiver unit ~~according to the species~~ achieves the objective in that at least one adder is connected to the output of the signal receivers for adding the analog received signals. The adder is connected by  
15 its output to the input of a common analog-digital converter. In this context, the following signal processing unit is configured to separate the added received signals.

Thus, it is provided to use a common analog-digital converter for the several signal receivers, the individual analog received signals being added up beforehand and the  
20 digital added-up common received signal being separated again later in the digital part. This has the advantage of requiring only a single analog-digital converter. The separation is readily possible in the digital part, particularly if the analog received signals having different receiving frequencies are added up. These may then be readily filtered out by the signal processing unit using channel filtering.

25 For this purpose it is advantageous if the signal receivers have mixers for mixing the analog received signals to different intermediate frequencies.

For this purpose, at least one signal receiver should be configured for mixing an  
30 analog received signal to intermediate frequency and at least one other signal receiver should be configured for receiving and filtering one defined receiving frequency band and adding this receiving frequency band to the intermediate frequency signal. In this manner, it is easy to separate the intermediate frequency signal in the digital signal receiver from the rest of the receiving frequency band. On

account of the digitalization of a defined receiving frequency band, it is easy to continue to monitor the range of receivable radio broadcast stations and to extract data from this receiving frequency band.

- 5 It is furthermore advantageous if the several signal receivers are provided for the simultaneous reception of analog received signals on different channels and/or according to different transmission standards. This makes it possible to receive different receiving channels and different standards and to process these further in the digital part of the receiver unit.

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Furthermore, ~~the~~an objective of the present invention is to create an improved method for receiving analog received signals that requires a lower expenditure for the receiver unit. Using the method of the ~~species~~present invention, the objective is attained by the steps:

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- simultaneous reception of several analog received signals,
- addition of the analog received signals,

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- analog-digital conversion of the addition-received signal, and
- separation of the digital addition-received signal into several digital received signals corresponding to the several analog received signals.

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~~In the following, the present invention is explained in detail by way of example with reference to the attached drawings. The figures show:~~

#### Brief Description Of The Drawings

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~~Fig. 1~~Figure 1 shows a block diagram of a conventional receiver unit having several signal receivers and in each case one analog-digital converter per signal receiver;

~~Fig. 2~~Figure 2 shows a block diagram of a receiver unit according to the present invention having several signal receivers and a common analog-digital converter.

### Detailed Description

Figure 1 shows a block diagram of a conventional receiver unit for the simultaneous reception of several analog receiver signals on the same or different receiving channels and/or according to the same transmission standards. Every signal receiver 2 has a prestage 3 having a band-pass filter 4 and a preamplifier 5. The input of prestage 3 is connected to an antenna 6 and the output of prestage 3 is in each instance connected to a mixer 7. Mixer 7 is triggered by an oscillator 8 so as to mix the analog received signal to an intermediate frequency ZF. The intermediate frequency signal ZF is filtered in an intermediate frequency filter 9 and is subsequently amplified using an amplifier 10.

Every signal receiver 2a has an analog-digital converter ADC for converting the amplified analog intermediate frequency signal into a digital received signal and subsequently process it further digitally using a signal processing unit (not shown).

Figure 2 shows a block diagram of a receiver unit according to the present invention having three signal receivers 2a, 2b and 2c. In a known manner, the two signal receivers 2a and 2b have a prestage 3, a mixer 7 having an oscillator 8, an intermediate frequency filter 9 and an amplifier 10. Third signal receiver 2c only has a prestage 3 having a band-pass filter 4 and a preamplifier 5 such that the receiving frequency band defined by band-pass filter 4 is transmitted at the receiving frequency and not at intermediate frequency ZF. Using adders 11, the analog received signals are then added up at the respective frequency to a common analog received signal, in which case different intermediate frequencies  $ZF_1$  and  $ZF_2$  should be used so as to be later readily able to split up the common received signal using digital channel filtering.

Using a common analog-digital converter ADC, the addition-received signal is converted into a digital received signal and is preprocessed in a subsequent digital signal processing unit 12. In digital signal processing unit 12, the individual received signals are again split up using channel filtering.

Abstract Of The Disclosure

A receiver unit (1), having at least two signal receivers (2a, 2b, 2c) for analog received signals, an analog-digital converter (ADC) for converting the analog received signals into digital received signals, and having a signal processing unit (12) for the digital received signals, has at least at least one adder (11) at the output of the signal receivers (2a, 2b, 2c) for adding the analog received signals, the adder (11) being connected with its output to the input of a common analog-digital converter (ADC) and the digital signal processing unit (12) being configured to separate the added received signals.

10    ~~Reference to Figure 2~~